# TOSHIBA

#### TOSHIBA Transistor Silicon PNP · NPN Epitaxial Type

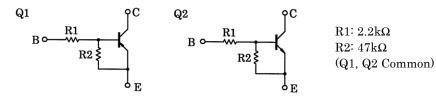
(PCT Process) (Bias Resistor Built-in Transistor)

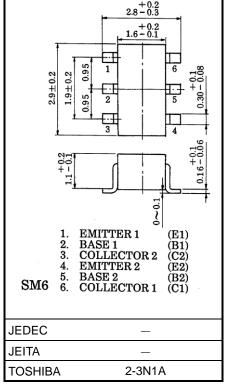


Switching, Inverter Circuit, Interface Circuit and Driver Circuit

- Including two devices in SM6 (super mini type with 6 leads)
- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process and miniaturize equipment.

#### **Equivalent Circuit and Bias Resistor Values**







#### Q1 Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	-50	V
Collector-emitter voltage	VCEO	-50	V
Emitter-base voltage	V <sub>EBO</sub>	-5	V
Collector current	IC	-100	mA

#### Q2 Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	50	V
Collector-emitter voltage	VCEO	50	V
Emitter-base voltage	VEBO	5	V
Collector current	lc	100	mA

Unit: mm

#### Q1, Q2 Common Absolute Maximum Ratings (Ta = 25°C)

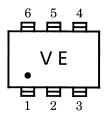
Characteristic	Symbol	Rating	Unit
Collector power dissipation	Pc *	300	mW
Junction temperature	Tj	150	°C
Storage temperature range	T <sub>stg</sub>	-55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

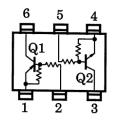
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

\* Total rating

#### Marking



#### **Equivalent Circuit (Top View)**



#### Q1 Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	ICBO	_	V <sub>CB</sub> = -50 V, I <sub>E</sub> = 0 mA	_	_	-100	nA
	ICEO	_	Vce = −50 V, IB = 0 mA	_	_	-500	
Emitter cut-off current	I <sub>EBO</sub>	_	$V_{EB} = -5 V, I_{C} = 0 mA$	-0.078	_	-0.145	mA
DC current gain	hFE	_	Vce = −5 V, Ic = −10 mA	80	_	_	—
Collector-emitter saturation voltage	VCE (sat)	_	IC = −5 mA, IB = −0.25 mA	_	-0.1	-0.3	V
Input voltage (ON)	VI (ON)	_	Vce = −0.2 V, Ic = −5 mA	-0.6	_	-1.1	V
Input voltage (OFF)	VI (OFF)	_	$V_{CE} = -5 V, I_C = -0.1 mA$	-0.5	_	-0.8	V
Transition frequency	f⊤	_	$V_{CE} = -10 \text{ V}, \text{ I}_{C} = -5 \text{ mA}$	_	200	_	MHz
Collector output capacitance	C <sub>ob</sub>	—	$V_{CB}$ =-10 V, IE = 0 mA, f = 1 MHz	_	3	6	pF

#### Q2 Electrical Characteristics (Ta = 25°C)

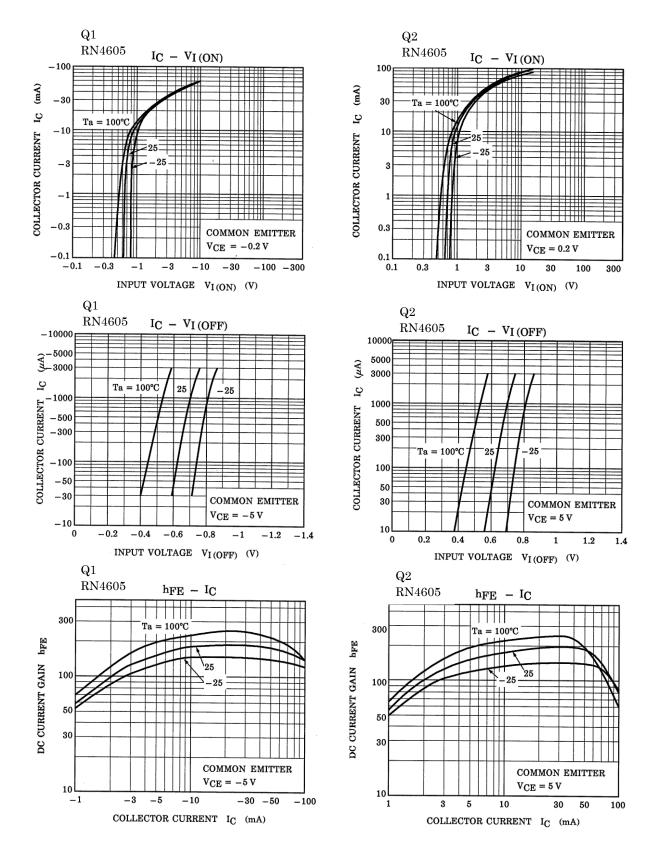
Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	ICBO	-	$V_{CB} = 50 \text{ V}, \text{ I}_{E} = 0 \text{ mA}$	—	_	100	nA
	ICEO	-	$V_{CE} = 50 \text{ V}, \text{ I}_{B} = 0 \text{ mA}$	—	_	500	
Emitter cut-off current	I <sub>EBO</sub>	-	$V_{EB} = 5 V, I_{C} = 0 mA$	0.078	_	0.145	mA
DC current gain	hFE	-	$V_{CE} = 5 V, I_{C} = 10 mA$	80	_	-	—
Collector-emitter saturation voltage	V <sub>CE</sub> (sat)	_	I <sub>C</sub> = 5 mA, I <sub>B</sub> = 0.25 mA	_	0.1	0.3	V
Input voltage (ON)	V <sub>I (ON)</sub>	-	$V_{CE} = 0.2 \text{ V}, \text{ I}_{C} = 5 \text{ mA}$	0.6	_	1.1	V
Input voltage (OFF)	VI (OFF)	-	$V_{CE} = 5 V, I_C = 0.1 mA$	0.5	_	0.8	V
Transition frequency	fT	-	$V_{CE} = 10 \text{ V}, \text{ I}_{C} = 5 \text{ mA}$	—	250	_	MHz
Collector output capacitance	C <sub>ob</sub>	_		_	3	6	pF

#### Q1, Q2 Common Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Input resistance	R1	_	—	1.54	2.2	2.86	kΩ
Resistance ratio	R1/R2	—	_	0.0421	0.0468	0.0515	—

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#### Q1,Q2 characteristics curves



The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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